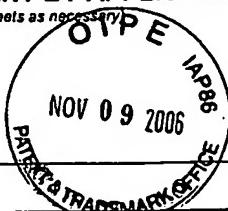


Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(Use as many sheets as necessary)</i>		<i>Complete if Known</i>	
		<b>Application Number</b>	10/765,301
		<b>Filing Date</b>	January 27, 2004
		<b>First Named Inventor</b>	Leslie, Terrence
		<b>Group Art Unit</b>	2815
		<b>Examiner Name</b>	Landau, Matthew
Sheet 1 of 1		Attorney Docket No: 303.860US1	

**US PATENT DOCUMENTS**

Examiner Initial*	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No.	Publication Date	Name of Patentee or Applicant of cited Document	T <sup>2</sup>

**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
M		GEPPERT, LINDA, "Triple Gate Double Play: Transistors with three gates and 3-D geometries will replace today's devices in a few years", <u>IEEE Spectrum</u> , (November 2002), 18	

EXAMINER

*Matthew C. Landau*

DATE CONSIDERED

2/12/2007

Substitute Disclosure Statement Form (PTO-1449)

\*EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached